

Debugging and Optimization of Scientific Applications

October 21st-23rd 2019
Cineca, Casalecchio di Reno

Instructors: A. Emerson, A. Marani, F. Ficarelli, C. Latini

October 21st

- 9.00-9.30 Registration and welcome
- 9.30-10.45 Fundamentals of computer architecture-1 (A.Emerson)
- 11.00-12.15 Fundamentals of computer architecture-2(A. Emerson)
- 13.00-14.00 LUNCH
- 14.00-14.45 Introduction to Cineca's HPC infrastructure (A. Marani)
- 14.45-17.00 Exercise: Running serial, MPI and OpenMP programs on Galileo (A. Marani, A.Emerson)

October 22nd

- 9.30-13.00 Compilers + serial optimisation Exercises (A. Emerson,F.Ficarelli)
- 13.00-14.00 LUNCH
- 14.00-15.30 Intranode optimisation (A. Emerson, F.Ficarelli)
- 15.30-17.00 Exercises

October 23rd

- 9.30-13.00 Performance Profiling + Exercises (A. Emerson)
- 13.00-14.00 LUNCH
- 14.00-17.00 Debugging, demonstration and Exercises (C. Latini)

Guided visits to the computer room of Cineca will also be organized.