

26th Summer School on Parallel Computing

Roma, 10-21 July 2017

	Monday	Tuesday	Wednesday	Thursday	Friday
9.00 10.50	Introduction to HPC (GE)	Concepts of Parallel Programming (GE)	MPI Parallel Programming (CT+MI)	Code Optimization (VR)	Debugging Techniques and Tools (VR)
Coffee Break					
11.10 13.00	Introduction to HPC Architectures (GE)	MPI Parallel Programming (CT+MI)	MPI Parallel Programming (CT+MI)	Code Optimization (NS)	Profiling Techniques and Tools (PL)
Lunch					
14.00 15.50	Software Engineering for HPC (PC - CINI)	CINECA HPC Programming Environment (NB)	Hands-on Session: MPI collective communications	MPI Datatypes Virtual Topologies (IB)	Hands-on Session: Debugging and Profiling
Coffee Break					
16.10 18.00	Software Engineering for HPC (PC - CINI)	Hands-on Session: MPI Point-to-Point Communications	Hands-on Session: MPI Collective Communications	Hands-on Session: MPI Collective Communications	Hands-on Session: Debugging and Profiling

	Monday	Tuesday	Wednesday	Thursday	Friday
9.00 10.50	OpenMP Programming (CP)	Hybrid Programming: MPI+OpenMP (FS)	Intel Xeon Phi Architecture (VR)	Introduction to GPU Accelerators and CUDA Programming (SO)	Introduction to scientific libraries (PL)
Coffee Break					
11.10 13.00	OpenMP Programming (CP)	Hybrid Programming: MPI+OpenMP (FS)	Intel Xeon Phi Vectorisation Techniques (VR)	Introduction to GPU Accelerators and CUDA Programming (SO)	HPC and Data Analytics (RZ)
Lunch					
14.00 15.50	Hands-on Session: OpenMP	Hands-on Session: Code Hybridization MPI+OpenMP	Hands-on Session: Code Hybridization MPI+OpenMP	Hands-on Session: GPU, CUDA Programming	FREE
Coffee Break					
16.10 18.00	Hands-on Session: OpenMP	Hands-on Session: Code Hybridization MPI+OpenMP	Hands-on Session: Code Hybridization MPI+OpenMP	Hands-on Session: GPU, CUDA Programming	FREE

GE=Giovanni Erbacci, CT=Claudia Truini, MI=Mariella Ippolito, VR=Vittorio Ruggiero, NS=Nico Sanna, AG=Alessandro Grottesi, RZ=Riccardo Zanella, PL=Piero Lanucara, IB=Isabella Baccarelli, FS= Francesco Salvatore, SO=Sergio Orlandini, PC=Paolo Ciancarini (Consorzio Interuniversitario Nazionale per l'Informatica, CINI)